



INFORMATION DISCLOSURE CITATION PTO-1449	Atty. Docket No. 970813	Serial No. 08/921,250
	Applicant(s): INOUE et al.	
	Filing Date: August 29, 1997	Group Art Unit: Unassigned (763)

U.S. PATENT DOCUMENTS

Examiner Initial	Document No.	Name	Date	Class	Subclass	Filing Date (If appropriate)
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_____	AA	MAR 29 2004				
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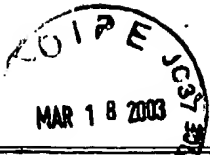
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Examiner Initial	Document No.	Date	Country	Translation (Yes or No)
<i>gag</i>	AC	63-198359	08/1988	Japan
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Examiner <i>George Goudreau</i> Date Considered <i>7-041</i>		



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<i>gag</i>	AA 5,892,269 ✓	Inoue et al.	4/1999			
	AB 6,235,648 ✓	Mizuhara et al.	5/2001			
	AC 6,288,438 ✓	Mizuhara et al.	9/2001			
<i>gag</i>	AD 5,459,086 ✓	Yang	10/1995			
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<i>gag</i>	AF 10-242278 ✓	9/11/98	Japan	Abstract
	AG 10-270447 ✓	10/09/98	Japan	Abstract
	AH 10-189724 ✓	7/21/98	Japan	Abstract
	AI 09-293783 ✓	11/11/97	Japan	Abstract
	AJ 10-242277 ✓	9/11/98	Japan	Abstract
<i>gag</i>	AK 09-069562 ✓	3/11/97	Japan	Abstract
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George A. Gondreau

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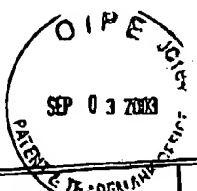
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	AM	Serial No. 09/320,271, Filed 5/27/99 (U. S. Publication No. 20010055873, Publication date 12/27/2001) ✓
	AN	Serial No. 09/228,148, Filed 1/11/99, Docket No. 970813. ✓
	AO	Serial No. 09/716,334, Filed 11/21/00, Docket No. 981187A. ✓
	AP	Wolf, et al. "Silicon Processing for the VLSI Era: Volume 1 - Process Technology," Lattice Press, 1986 p.441. ✓
	AQ	S. M. Sze, Physics of Semiconductor Device, "A Wiley Interscience publication.", p. 393, 1981. ✓
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gag	AA	5,581,101 ✓	Ning et al.	12/1996	257	347	
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gag	AF	6-291202 ✓	10/1994	Japan	
	AG	63-199859 ✓	8/1998	Japan	
gag	AH	56-125844 ✓	10/81	Japan	
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	AB	5,960,321 ✓	Hsieh et al.	9/1999			
	AC	6,071,810 ✓	Wada et al.	6/2000			
	AD	6,136,686 ✓	Gambino et al.	10/2000			
	AE	6,153,519 ✓	Jain et al.	11/2000			

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Examiner <u>George Goudreau</u>	Date Considered <u>3-04-1</u>
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